

AMENDMENTS TO THE SPECIFICATION:

Please revise the final paragraph on page 12, as follows:

Referring to FIG. 1, reference numerals 12, 13, 14, and 15 indicate PMOS transistors corresponding to the PMOS transistors 1, 2, 3, and 4 shown in FIG. 6, respectively. In FIG. 1, reference numerals 16, 17, 18, 19, 20, 23, and 24 indicate NMOS transistors corresponding to the NMOS transistors 5, 6, 7, 8, 9, 10, and 11 shown in FIG. 6, respectively. Referring to FIG. 1, reference numerals ~~12~~ 21 and 22 indicate NMOS transistors that function as a feedthrough-current blocking means in combination, which are not included in the configuration shown in FIG. 6.

Please replace the Abstract with the version on the next page.